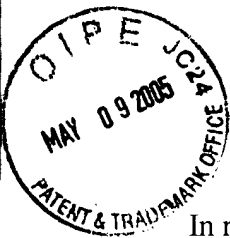


IFW



IR-2076 (2-2689)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Kohji ANDOH et al.

Date: May 5, 2005

Serial No.: 10/728,482

Group Art Unit: 2812

Filed: December 4, 2003

Examiner: A. Stevenson

For: PROCESS FOR PREPARATION OF SEMICONDUCTOR WAFER SURFACE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

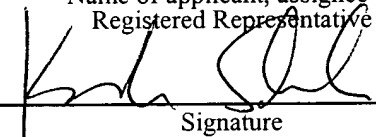
In response to the Notice of Non-compliant Amendment dated April 22, 2005, a corrected copy of the Listing Of The Claims Section of Applicant's communication filed April 14, 2005 is attached, in which the status of all of the claims is indicated.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, on May 5, 2005

Respectfully submitted,

Kourosh Salehi


Name of applicant, assignee or
Registered Representative


Signature

May 5, 2005

Date of Signature

KS:gl



Kourosh Salehi

Registration No.: 43,898

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700